# SPECIFICATION FOR COG MODULE

**ORDER NO: TMTG320240D-02** 

MODULE NO.: TMTG320240D-F-FC-LED05-W

#### **DOC.REVISION A01**

Customer Approval:		

	NAME	SIGNATURE	DATE
PREPARED BY	KOWK SHING FU		19 JUN 2007
CHECKED BY	FU SZE HOI		19 JUN 2007
APPROVED BY	FU SZE HOI		19 JUN 2007

# **DOCUMENT REVISION HISTORY**

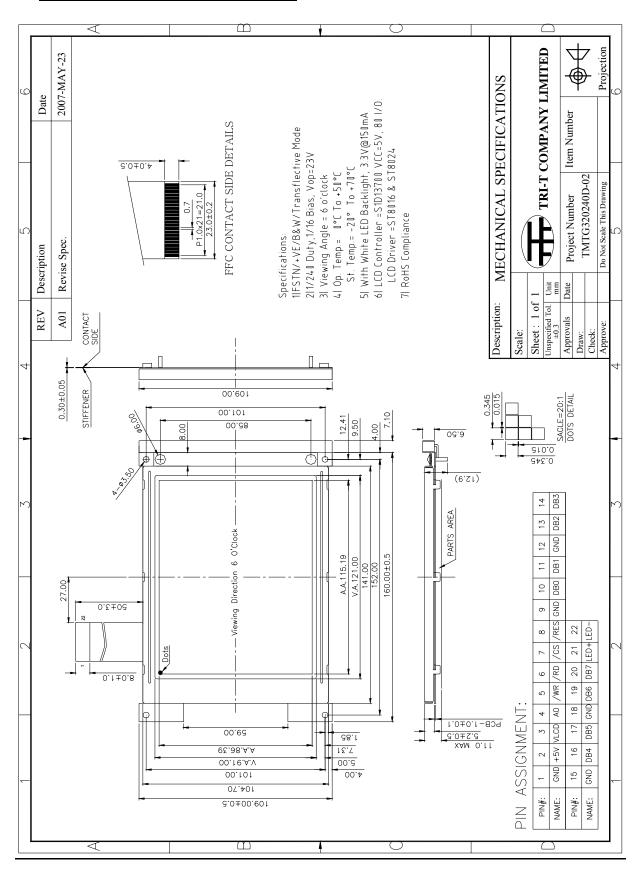
Version	DATE	CHANGED BY	
A00	06 OCT 2005	<b>DESCRIPTION</b> First issue	CHARGED D1
Au	00 001 2003	1 Hist issue	
A01	19 JUN 2007	Update mechanical drawing on page 1	Kowk Shing Fu



<b>Dimensional Outline</b>	1
Functions & Features	2
<b>Mechanical Specifications</b>	2
Block Diagram	3
Pin Description	4
Power Supply	5
Maximum Absolute Limit	6
Electrical Characteristics	6 ~ 9
<b>Backlight Specification</b>	10
Control and Display Commands	11 ~12
Display Data RAM Addressing	13
Electro-Optical Characteristics	14
<b>Quality Specifications</b>	15-22



### 1. <u>DIMENSIONAL OUTLINE</u>





### 2.FUNCTIONS & FEATURES

2-1. Format : 320\*240 dots

2-2. LCD Mode : FSTN/+VE/Transflective Mode

2-3. Viewing Direction : 6 o'clock

2-4. Driving Scheme : 1/240 Duty cycle, 1/16 Bias

2-5. Single Supply Voltage : Power supply voltage range ( $V_{DD}$ ): 2.7~5.5V

2-6. With LED Backlight, Color : White

2-7. 8080 Serie Parallel Interface2-10. On-Board DC-DC Converter

2-11. RoHs Compliance

### **3.MECHANICAL SPECIFICATIONS**

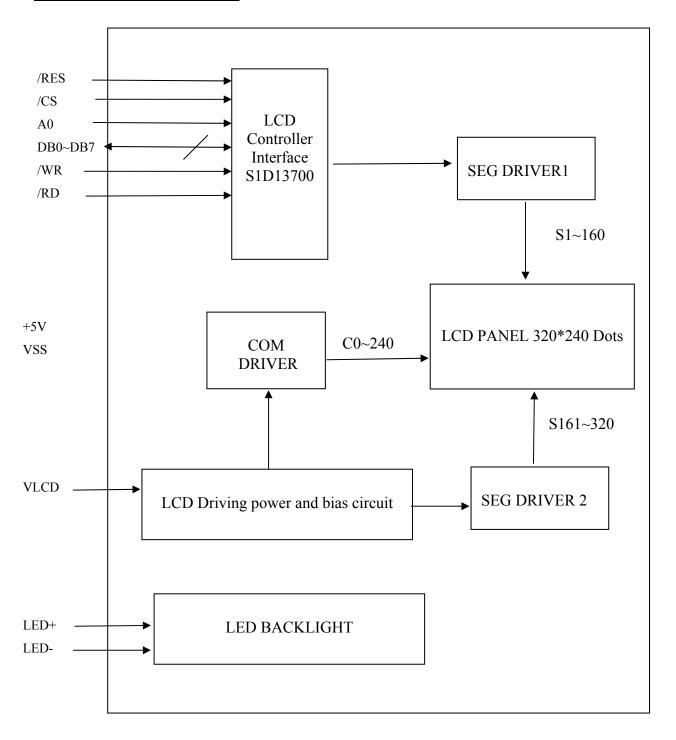
3-1. Module size : 167.1mm(L)\* 109.0mm(W) \*11.0mm(D)(not include FPC)

3-2. Viewing area : 121.0mm(L)\* 91.0mm(W)
3-3. Active area : 115.19mm(L)\* 86.39mm(W)
3-4. Dot pitch : 0.36mm(L)\*0.36mm(W)
3-5. Dot size : 0.345mm(L)\*0.345mm(W)

. 0.3 (3 mm(L) 0.3 (3 mm)(V)



# **4.BLOCK DIAGRAM**

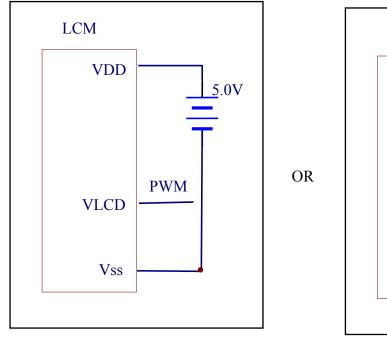


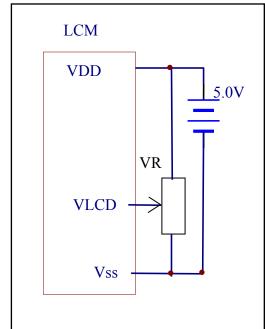


# **5. PIN DESCRIPTION**

	1	
1	VSS	Ground
2	+5V	Supply voltage for logic circuit
3	VLCD	LCD contrast control pin,
4	A0	Data type select
5	/WR	8080 family: Read signal, low active
6	/RD	8080 family: Write signal, low active
7	/CS	Chip select
8	/RES	Reset
9	GND	Ground
10	DB0	8-bit bi-directional data bus
11	DB1	8-Dit di-difectional data dus
12	GND	
13	DB2	8-bit bi-directional data bus
14	DB3	8-Dit di-difectional data dus
15	GND	Negative voltage supply pin
16	DB4	8-bit bi-` data bus
17	DB5	o-vit vi- uata vus
18	GND	Ground
19	DB6	8-bit bi-directional data bus
20	DB7	o-oit or-uncetional data ous
21	LED+	Anode of LED backlight, +3.3V
22	LED-	Cathode of LED backlight, 0V

### 6. POWER SUPPLY





i)PWM= Pulse Width Modulation, 50% duty cyclue

ii) VR=47 kohm

VLCD is the control pin for adjusting the LCD contrast.



# **7.MAXIMUM ABSOUTE LIMIT (T=25°C)**

Item	Symbol	Standard value	Unit
Supply Voltage	CORE VDD	VSS-0.3 to +4.0	V
Supply Voltage	IO VDD	Vss-0.3 to +7.0	V
Input voltage	V <sub>IN</sub>	Vss-0.3 to IO VDD +0.5	V
Output Voltage	Vout	Vss-0.3 to IO VDD +0.5	V
Operating temperature	Topr	0~+50	°C
Storage temperature	Tstg	-20~+70	°C

Note: Voltage greater than above may damage the module

**8.ELECTRICAL CHARACTERISTICS 8-1.DC Characteristics** ( $V_{DD}$ = 4.5 to 5.5V,  $V_{SS}$ = 0V,  $T_a$ = -20 to 75°C)

Symbol	Parameter		]	Rating	Test condition	
Symbol	1 arameter	Min Typ Max		Max	Unit	
Core V <sub>DD</sub>	Supply Voltage	3.0	3.3	3.6	V	VSS=0V
$HIO V_{DD}$	Host Bus IO Supply Voltage	4.5	5.0	5.5	V	VSS=0V
$HIO\ V_{DD}$	Panel supply voltage	4.5	5.0	5.5	V	VSS=0V
$I_{DD}$	Logic current	-	30	50	mA	DC/DC Converter On
HIO V <sub>IN</sub>	Host Input Voltage	VSS	-	HIO V <sub>DD</sub>	V	
NIO V <sub>IN</sub>	Non-Host Input Voltage	VSS	-	NIO VDD	V	
$I_{LZ}$	Input Leakage Current	-1	-	1	uA	
l <sub>oz</sub>	Output Leakage Current	-1	-	1	uA	
$V_{OH}$	High Level Output Voltage	VDD- 0.4	-	-	V	VDD=min. IOH=-6mA
$V_{OL}$	Low Level Output Voltage	-	-	0.4	V	VDD=min. IOH= 6mA
V <sub>IH1</sub>	High Level Input Voltage	2.0	-	-	V	LVTTL LEVEL, VDD=max
$V_{IL1}$	Low Level Input Voltage	-	-	0.8	V	LVTTL LEVEL, VDD=min.
$V_{T+}$	High Level Input Voltage	1.1	-	2.4	V	LVTTL Schmitt
V <sub>T-</sub>	Low Level Input Voltage	0.6	-	1.8	V	LVTTL Schmitt
V <sub>H1</sub>	Hysteresis Voltage	0.1	-	-	V	LVTTL Schmitt
R <sub>PD</sub>	Pull Down Resistance	2.0	50	120	K ohm	



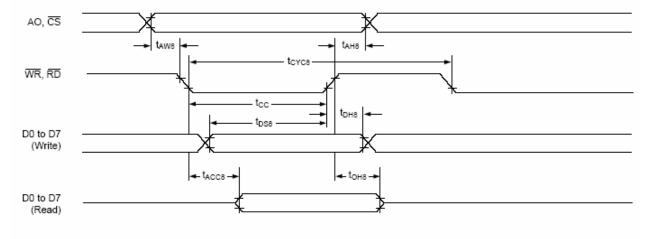
#### 8-2-1. Parallel Read/Write Characteristics (8080-series)

 $Ta = -20 \text{ to } 75^{\circ}C$ 

Signal	Symbol	Parameter	VDD = 4.	.5 to 5.5V	VDD = 2	.7 to 4.5V	Unit	Condition		
Signai	Symbol	1 at affected	min	max	min	max	Omi	Condition		
40 CC	tAH8	Address hold time	10	_	10	_				
A0, CS	tAW8	Address setup time	0	_	0	_				
	tCYC8	System cycle time	See note.	_	See note.	_				
WR, — RD	tCC	Strobe pulsewidth	120	_	150	_	ns	CL = 100pF		
	tDS8	Data setup time	120		120	_				
D0 to	tDH8	Data hold time	5	_	5	_		l		
D7	tACC8	RD access time	_	50	_	80				
	tOH8	Output disable time	10	50	10	55				

Note: For memory control and system control commands: tCYC8 =2tC + tCC + tCEA +75 > tACV + 245 For all other commands: tCYC8 = 4tC+ tCC + 30

#### 8-2-2 8080 family interface timing





### 9.BACKLIGHT SPECIFIATIONS

### 9-1. Absolute maximum rating

Item	Symbol	Ratings	Unit
Peak forward current	Ifp	250	mA
Reverse voltage	Vr	1.0	V
Power dissipation	Pd	1200	mW

### 9-2. Electrical specifications

Item	Symbol	Min	Туре	Max	Unit	Conditions
Luminous intensity	Lv	-	150	-	cd/m <sup>2</sup>	
Peak emission wavelength	λр	-	-	-	ηm	IF=150mA
Spectral line half width	Δλ	-	-	-	ηm	Ta=25°C
Forward voltage	Vf	3.0	3.3	3.6	V	
Reverse current	Ir	-	35	-	mA	VR=0.8V



### 10.CONTROL AND DISPLAY COMMAND

G1						С	ode								No.
Class	Command	— RD	— WR	A 0	D 7	D 6	D 5	D 4	D 3	D 2	D 1	D 0	Hex	Command Description	of Bytes
System	SYSTEM SET	1	0	1	0	1	0	0	0	0	0	0	40	Initialize device and display	8
control	SLEEP IN	1	0	1	0	1	0	1	0	0	1	1	53	Enter standby mode	0
	DISP ON/OFF	1	0	1	0	1	0	1	1	0	0	D	58, 59	Enable and disable display and display flashing	1
ļ	SCROLL	1	0	1	0	1	0	0	0	1	0	0	44	Set display start addressand display regions	10
	CSRFORM	1	0	1	0	1	0	1	1	1	0	1	5D	Set cursor type	2
Display control	CGRAM ADR	1	0	1	0	1	0	1	1	1	0	0	5C	Set start address of character generator RAM	2
	CSRDIR	1	0	1	0	1	0	0	1	1	C D 1	C D 0	4C to 4F	Set direction of cursor movement	0
	HDOT SCR	1	0	1	0	1	0	1	1	0	1	0	5A	Set horizontal scroll position	1
	OVLAY	1	0	1	0	1	0	1	1	0	1	1	5B	Set display overlay format	1
Drawing	CSRW	1	0	1	0	1	0	0	0	1	1	0	46	Set cursor address	2
control	CSRR	1	0	1	0	1	0	0	0	1	1	1	47	Read cursor address	2
Memory	MWRITE	1	0	1	0	1	0	0	0	0	1	0	42	Write to display memory	_
control	MREAD	1	0	1	0	1	0	0	0	0	1	1	43	Read from display memory	_

#### Notes:

- 1. In general, the internal registers of the SED1335 series are modified as each command parameter is input. However, the microprocessor does not have to set all the parameters of a command and may send a new command before all parameters have been input. The internal registers for the parameters that have been input will have been changed but the remaining parameter registers are unchanged.
  - 1.2-byte parameters (where two bytes are treated as 1 data item) are handled as follows:

    - CSRW, CSRR: Each byte is processed individually. The microprocessor may read or write just the low byte of the cursor address. SYSTEM SET, SCROLL, CGRAM ADR: Both parameter bytes are processed together. If the command is changed after half of the b. parameter has been input, the single byte is ignored.
- 2. APL and APH are 2-byte parameters, but are treated as two 1-byte parameters.

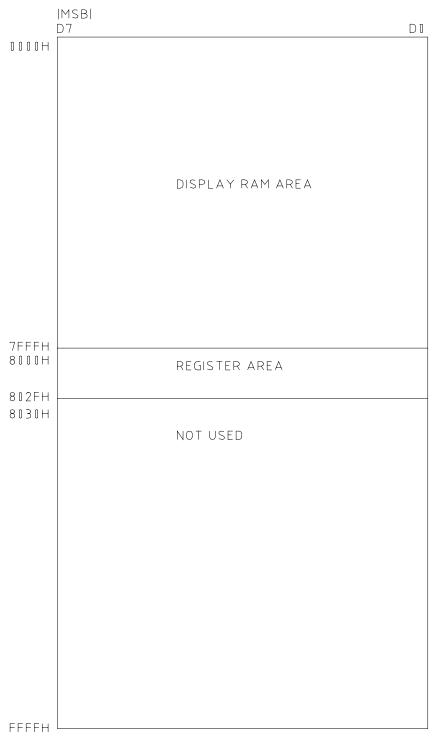


# 11.INTERNAL CHARACTER GENERATOR FONT

							CI	harac	ter co	de bit	ts 0 to	3					
		0	1	2	3	4	5	6	7	8	9	Α	В	С	D	Ε	F
	2			H	#		#.: <u>.</u>		:		3	<b>:</b>	<b></b>	;			.••
	3				3	<b>:</b>		≝.	7			::	# ;			>	
	4										I			<u> </u>			
2.0	5				S			l,,l		X	¥	<u></u>				•	
Character code bits 4 to	6	•	.==		===		===	#	-===	ŀ		••••	<u></u>	1.			
r code	7	<b></b>	•==	<b>!</b>	₩	-	<b></b>		إبرا	×	•===		الى <sub>نىد</sub> ا		7	-∰-	<b>÷</b>
haracte	Α		<b>:::</b>	<b>!</b>		•	::	<b>::::</b>		·4		<u></u>	<b>#</b>	<b>::::</b>			• • • •
ਹ	В		<b>!</b> ''	•4	<b></b>						•		<b>#::"</b> .	:: <u>.</u> :			
	С		#	•	<b>:</b>	<b>!</b>	<u>.</u>		.::	#	ļ	·			••••	#	;
	D	<u>-</u>	<u>:</u>	.:: <sup>:</sup>	===	-				ij					 :	÷	<b>III</b>
	1																



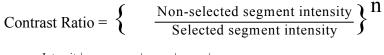
### 11.1. MEMORY MAPPING

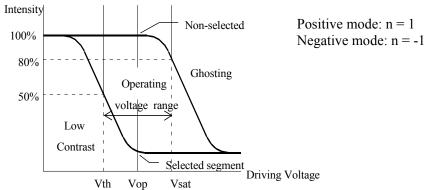


#### 12. ELECTRO-OPTICAL CHARACTERISTICS

Item		Temp Unit		Standard Value			Note
		Temp	Omi	Min	Тур	Max	Note
Operating Voltage	Vop	25℃	V		23		
Frame Frequency	f	25℃	Hz	64	75	100	(1)
Danasa Tima	Ton	25℃	maaa	-	200	250	(2)
Response Time	Toff	25℃	Hisec	msec -	250	300	
Contrast Ratio(θ= 30°)	CR	25℃		2	4.5	-	
Viewing Angle(CR>2)	θ	25°C	Deg	20	30	-	

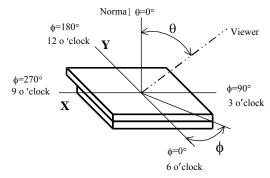
#### **Contrast Ratio Definition**





#### **Viewing Angle Definition**

- $\theta$ : Angle between Viewer Direction and Normal. (  $-90^{\circ} \le \theta \le 90^{\circ}$  )
- $\phi$  : Angle between Projection of Viewer Direction to X-Y plane and Y axis. (  $0^\circ\!\!\le\!\!\phi\!\!\le\!\!360^\circ)$



#### Measuring Condition

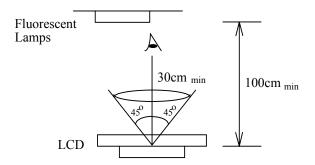
- 1. Driving Voltage: Same as Vop.
- 2. Driving Frequency: Same as Frame Frequency.



### 13. Quality Specifications

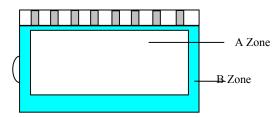
#### 13.1 Inspection Condition

13.1.1 The inspection shall be performed by using  $20W \times 2$  fluorescent lamps . Distance between LCD and fluorescent lamps should be 100 cm or more. Distance between LCD and inspector eyes should be 30 cm or more.



- 13.1.2 For transmissive displays a reflector (e.g. a white card) shall be placed behind the display.
- 13.1.3 Viewing direction for inspection is 45° from vertical against LCD.

#### 13.2. Definition of Zone



A Zone: Active display area (minimum viewing area). B Zone: Non-active display area (outside viewing area).



### 13.3 Sampling Method: MIL-STD-105E.

# 13.4 Inspection level: Level II, Single Sampling.

Rank	Item Criterion			AQL
	1.Display state	<ol> <li>Segment short circuit.</li> <li>Open circuit (missing segment)</li> <li>Contrast defect (dim, ghosting)</li> </ol>	1	0.4
Major		4. Segment defect (Pin hole, etc.)	6	
3		5. Leakage	/	Ac:0
		6. No display, polarizers reverse applied		Re: 1
	2. Dimension	1. Dimension out of specification (incl. PIN)	2	0.4
	1. Non-display state	<ol> <li>Spot, foreign material, line defect</li> <li>Rainbow, background color</li> </ol>	3, 4, 5	
Minor	2. Polarizer	<ol> <li>Scratch</li> <li>Bubble</li> <li>Foreign material</li> </ol>	3, 4, 7	
		4. Poor fixed position	8	
	3. Glass	1. Chipped	9	1.0
	substrate	2. Protruded, burred	10	
4. PIN		Positioning     Epoxy coverage	11	-
	5. Silk screen	Positioning     Color	12	-
		3. Semblance defect (Refer spot, line Standards)	3, 4	
Total		1		1.0



### 13.5 Inspection Items and Standards

No	Item	Criterion			
1	Segment short, missing	Not exist			
	Contrast defect	Refer to approved sample			
2	Outside defect Positioning Color	Not exceed tolerance Refer to approved sample			
3	Point defect, Black spot, dust (incl. Polarizer) $\phi = (X+Y)/2$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			
4	Line defect	$\begin{array}{c ccccc} & & & & & & \\ \hline & & & & & \\ \hline & & & & \\ L & & & & \\ \hline & & & & \\ L & & & & \\ \hline & & & & \\ L & & & & \\ \hline & & & & \\ L & & & & \\ \hline & & & & \\ L & & & & \\ \hline & & & & \\ L & & & & \\ \hline & & & & \\ L & & & & \\ \hline & & & & \\ L & & & & \\ \hline & & & & \\ L & & & & \\ \hline & & & & \\ L & & & & \\ \hline & & & & \\ L & & & & \\ \hline & & & & \\ & & & \\ \hline & & & &$			
5	Rainbow	Not more than two color changes across the viewing area.  Background color should refer to approved sample.			
6	Segment pattern $W = Segment \ width$ $\phi = (A+B)/2$	1. Pin hole $\phi < 0.10 \text{mm is acceptable.}$ $\begin{array}{c ccccccccccccccccccccccccccccccccccc$			



No	Item	Criterion		
		2. Segment width acceptable.    A - B   < 0.20; D≤0.2  C≤0.1 E≤0.15		
		$\begin{array}{c c} & & & & & \\ \hline & & & & & \\ \hline A \Rightarrow & & & \\ \hline & \leftarrow \Rightarrow & & \\ \hline & \leftarrow B & & \\ \hline & E \Rightarrow \parallel \leftarrow \Rightarrow \parallel \leftarrow E \\ \hline \end{array}$		
		3. Dot pattern		
		Size Acceptable Qty. $\phi < 0.1$ Disregard $\phi > 0.20$ 0  C: Shall not touch other dot(s).  Unit: mm		
7	Polarizer air bubble	$\begin{array}{c cccc} Size & Acceptable Qty. \\ \hline \phi < 0.10 \text{ or B Zone} & Disregard \\ \hline 0.10 < \phi \leqslant 0.20 & 2 \\ \hline 0.20 < \phi \leqslant 0.50 & 1 \\ \hline 0.50 < \phi & 0 \\ \hline Total & 2 \\ \hline Unit: mm \\ \end{array}$		
8	Polarizer mis-placement	Polarizer should neither extends to glass edge nor extends into seal.		



No	Item	Criterion			
9	Crack and chip Remark:     X: Length     direction     Y: Short     direction     Z: Thickness     direction     t: Glass thickness	1. General $ \begin{array}{c ccccccccccccccccccccccccccccccccccc$			
	a: LCD length W: Terminal Width F: seal width	2. Corner  Acceptable criterion $X  Y  Z$ $\leq 5 \text{mm}$ shall not reach to $\leq t$ $Z$ 3. Crack on terminal			
		Acceptable criterion $\frac{X}{\sqrt{2}} = \frac{X}{\sqrt{2}} = \frac{X}{2$			
		$\begin{array}{c ccccccccccccccccccccccccccccccccccc$			



No	Item	Criterion
10	Protruded W: Terminal Width	Acceptable criteria: $Y \le 1/5W$
11	Pin	1. Positioning  2 Epoxy coverage  A, B Epoxy Maximum height shall extend above front rear polarizer surface  3. No stain, rust nor discoloration of the insulating portion shall be allowed.
12	Total no. of acceptable defect	A. Zone  Maximum 4 non-conformities per one unit.  Defect distance: each point to be separated over 5mm  B. Zone  It is acceptable when it is no trouble for quality and assembly in customer's end product.



#### 13.6 Reliability Standards

- 1 Under normal operating and storage conditions a lifetime of 50,000 hours is expected.
- 2 Reliability test condition:

Item	Condition	Time(hrs)	Assessment
High temp. storage	+70°C	240	
Low temp. storage	-20°C	240	No
Humidity	40°C / 90%RH	240	abnormalities in functions and appearance
Change of temperature	$-20^{\circ}\text{C} \leftarrow 25^{\circ}\text{C} \rightarrow 70^{\circ}\text{C}$ $(60 \text{ min} \leftarrow 5 \text{ min} \rightarrow 60 \text{min})$	5 cycles	

3. In case of mass production, every order shall draw 10pcs. of samples to conduct a reliability test in according to the following condition:

Item	Condition	Time (hrs)
High temp. storage	+70°C	48
Low temp. storage	-20°C	48
Humidity	40°C / 90%RH	48

4. Recovery time should be 24 hours minimum.

#### 13.7. Cautions for use

#### Pay attention to following points of handling the TAB LCD module:

- 1. Take utmost care when handling as these products are made of glass. Any strong mechanical impact due to falling, etc. may cause damage(breakage or cracking).
- 2. Polarizer is made of soft material and is easily damaged, take utmost care when handling. The protective film attached is to prevent scratch and protect against dirt, it is recommended that this film should be kept sealing before use.



- 3. Clean polarize with a soft cloth.
- 4. Do not touch the connection terminals of the display with bare hand, it will cause disconnection or defective insulation of terminals
- 5. Avoid use or extended storage at high temperature and high humidity. For extended storage, select a storage area where the temperature is 20±8℃ and the relative humidity is less than 65%
- 6. Do not expose the display to the direct sunlight or UV light.
- 7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

#### **Limited Warranty**

TRI-T modules are not consumer products, but may be incorporated by TRI-T's customers into consumer products or components thereof, TRI-T does not warrant that its modules and components are fit for any such particular purpose.

- The liability of TRI-T is limited to repair or replacement on the terms set forth below. TRI-T
  will not be responsible for any subsequent or consequential events or injury or damage to any
  personnel or user including third party personnel and/or user. Unless otherwise agreed in
  writing between TRI-T and the customer, TRI-T will only replace or repair any of its LCD
  which is found defective electrically or visually when inspected in accordance with TRI-T
  QUALITY INSPECTION STANDARD.
- 2. No warranty can be granted if any of the precautions state in handling liquid crystal display above has been disregarded. Broken glass, scratches on polarizer mechanical damages as well as defects that are caused accelerated environment tests are excluded from warranty.
- 3. In returning the module, they must be properly packaged; there should be detailed description of the failures or defect.